

10/530883 #3

JC12 PCT/PTC 11 APR 2005

IN THE UNITED STATES DESIGNATED OFFICE (DO/US)

In Re the Application of:)
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SUGAWARA, Yoshitaka)
)
Int'l. Serial No.: PCT/JP2004/011936)
)
Int'l. Filing Date: August 19, 2004)
)
Priority Date: August 22, 2003)
)
U.S. Serial No.: Not yet assigned)
)
For: "SEMICONDUCTOR DEVICE)
AND METHOD OF PRODUCING)
THE SAME, AND POWER)
CONVERSION APPARATUS)
INCORPORATING THIS)
SEMICONDUCTOR DEVICE")
)
)
Atty. File No.: 3688KE-1)

INFORMATION DISCLOSURE
STATEMENT

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Dear Sir:

The references cited on attached Form PTO-1449 are being called to the attention of the Examiner.

- ☒ Copies of the cited non-patent and/or foreign references are enclosed herewith.
- ☐ Copies of the cited U.S. patents and/or patent applications are enclosed herewith.
- ☐ Copies of the cited U.S. patents/patent application publications are not enclosed in accordance with the waivers dated July 11, 2003 and October 19, 2004, whereby patent applications filed, regardless of the filing date, need not submit copies of U.S. patents and U.S. patent application publications.
- ☐ Copies of the cited references are not enclosed, in accordance with 37 C.F.R. 1.98(d), because the references were cited by or submitted to the U.S. Patent and Trademark Office in prior application Serial No. _____ filed _____, which is relied upon for an earlier filing date under 35 U.S.C. § 120.

☒ To the best of applicants' belief, the pertinence of the foreign-language references are believed to be summarized in the attached English abstracts and in the figures, although applicants do not necessarily vouch for the accuracy of the translation.

☐ Examiner's attention is drawn to the following co-pending applications, copies of which have been or are being submitted:

Serial No. _____ filed _____

Serial No. _____ filed _____

☒ Other: International Search Report of related PCT Application Serial No. PCT/JP2004/011936.

Submission of the above information is not intended as an admission that any item is citable under the statutes or rules to support a rejection, that any item disclosed represents analogous art, or that those skilled in the art would refer to or recognize the pertinence of any reference without the benefit of hindsight, nor should an inference be drawn as to the pertinence of the references based on the order in which they are presented. Submission of this statement should not be taken as an indication that a search has been conducted, or that no better art exists.

It is respectfully requested that the cited information be expressly considered during the prosecution of this application and the references made of record therein.

FEES

<input checked="" type="checkbox"/>	<p>37 CFR 1.97(b): No fee is believed due in connection with this submission, because the information disclosure statement submitted herewith is satisfies one of the following conditions ("X" indicates satisfaction):</p> <ul style="list-style-type: none"> <input type="checkbox"/> Within three months of the filing date of a national application other than a continued prosecution application under 37 CFR 1.53(d), or <input type="checkbox"/> Within three months of the date of entry into the national stage of an international application as set forth in 37 CFR 1.491 or <input checked="" type="checkbox"/> Before the mailing date of a first Office Action on the merits, or <input type="checkbox"/> Before the mailing of a first Office action after the filing of a request for continued examination under 37 CFR 1.114. <p>Although no fee is believed due, if any fee is deemed due in connection with this submission, please charge such fee to Deposit Account 19-1970.</p>
<input type="checkbox"/>	<p>37 CFR 1.97(c): The information disclosure statement transmitted herewith is being filed after all the above conditions (37 CFR 1.97(b)), but before the mailing date of one of the following conditions:</p> <ul style="list-style-type: none"> (1) a final action under 37 C.F.R. 1.113 or (2) a notice of allowance under 37 C.F.R. 1.311, or (3) an action that otherwise closes prosecution in the application. <p>This Information Disclosure Statement is accompanied by:</p> <ul style="list-style-type: none"> <input type="checkbox"/> A Certification (below) as specified by 37 C.F.R. 1.97(e). Although no fee is believed due, if any fee is deemed due in connection with this submission, please charge such fee to Deposit Account 19-1970. <p style="text-align: center;">OR</p> <ul style="list-style-type: none"> <input type="checkbox"/> Please charge Deposit Account 19-1970 in the amount of \$180.00 for the fee set forth in 37 C.F.R. 1.17(p) for submission of an information disclosure statement. Please credit any overpayment or charge any underpayment to Deposit Account 19-1970.

<input type="checkbox"/>	<p>37 CFR 1.97(d): This Information Disclosure Statement is being submitted after the period specified in 37 CFR 1.97(c).</p> <p><input type="checkbox"/> This information Disclosure Statement includes a Certification (below) as specified by 37 C.F.R. 1.97(e) AND</p> <p><input type="checkbox"/> Applicants hereby requests consideration of the reference(s) disclosed herein. Please charge Deposit Account 19-1970 in the amount of \$180.00 under 37 C.F.R. 1.17(p). Please credit any overpayment or charge any underpayment to Deposit Account 19-1970. Election to pay the fee should not be taken as an indication that applicant(s) cannot execute a certification.</p>
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Certification (37 C.F.R. 1.97(e))
(Applicable only if checked)

- ☐ The undersigned certifies that:
- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement. 37 C.F.R. 1.97(e)(1).
 - ☐ A copy of the communication from the foreign patent office is enclosed.
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- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 C.F.R. 1.56(c) more than three months prior to the filing of this statement. 37 C.F.R. 1.97(e)(2).

Respectfully submitted,

SHERIDAN ROSS P.C.

By: 

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Date: April 11, 2005

10/530883

JC121000d PCT/PTC 11 APR 2005

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT****Complete if Known**

INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number		
				Filing Date		
				First Named Inventor		Yoshitaka Sugawara
				Art Unit		
				Examiner Name		
Sheet	1	of	1	Attorney Docket Number		3688KE-1

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number Number-kind Code ^{2 (if known)}	Publication Date MM-DD-YYYY	Name of Patentee of Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-			
		US-			
		US-			
		US-			

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴ ; Kind Code ⁵ (if known)				
		JP	- 08-018030	01/19/1996	Fuji Electric Co., Ltd.		
		JP	- 09-148681	06/06/1997	Sumitomo Electric Ind. Ltd.		
		JP	- 10-022495	01/23/1998	Meidensha Corp.		
		JP	- 2001-217363	08/10/2001	Hitachi Ltd.		
		JP	- 2002-325355	11/08/2002	The Kansai Electric Power Co., Inc.		
		JP	- 2001-325427	11/08/2002	The Kansai Electric Power Co., Inc.		
			-				

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

Examiner Initials*	Cite No. ¹	
		"4H-SiC HIGH POWER SIJFET MODULE" by Sugawara et al., ISPSD 2003, April 14-17, Cambridge, UK, pp. 127-130.
		"Structure of Stacking Faults Formed Durig the Forward Bias of 4H-SiC p-i-n Diodes" by Twigg et al., Applied Physics Letters, Vol. 82, No. 15, 14 April 2003, pp. 2410-2412
		"Recent Progress in SiC Power Device Developments and Application Studies" by Sugawara, ISPSD, April 14-17, 2003, Cambridge, UK., pp. 10-18.
		"4H-SiC p-n Diodes and Gate Turnoff Thyristors for High-Power, High-Temperature Applications" by Agarwal et al., Pergamon, Solid State Electronics 44 (2000) pp. 303-308.

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference is considered, whether or not citation is in conformance and not considered. Include copy of this form with next communication to applicant.